

UNISONIC TECHNOLOGIES CO., LTD

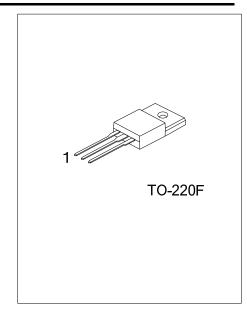
BTA16 Preliminary TRIAC

16A TRIACS

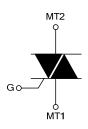
DESCRIPTION

The UTC BTA16 is a 16A triacs which can be operated in 4 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC BTA16 is suitable for AC switching application and phase control application such as fan speed and temperature modulation control, lighting control and static switching relay, either in through-hole or surface-mount packages.

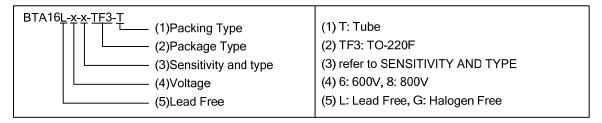


SYMBOL



ORDERING INFORMATION

Ordering Number		Dookogo	Pin .	Assignn	Doolsing		
Lead Free	Halogen Free	Package	1	2	3	Packing	
BTA16L-x-x-TF3-T	BTA16G-x-x-TF3-T	TO-220F	MT1	MT2	G	Tube	



SENSITIVITY AND TYPE

VOLTAGE		ΓAGE	OENOITIV/ITV	TVDE		
PART NUMBER	600V	800V	SENSITIVITY	TYPE		
В	0	0	50mA	STANDARD		
С	0		25mA	STANDARD		

⊚: Available

MARKING INFORMATION

PACKAGE	MARKING				
TO-220F	UTC BTA16☐ → C: Lead Free → G: Halogen Free → Data Code				

www.unisonic.com.tw 1 of 3

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full Sine Wave) T _C =86°C		I _{T(RMS)}	16	Α	
Non Repetitive Surge Peak On-State Current (Full	1 -30 112 11-201113		l	160	Α
Cycle, T _J initial=25°C)	F=60 Hz	t=16.7ms	I _{TSM}	168	Α
I ² t Value for Fusing	t _P =10ms		l ² t	144	A^2s
Critical Rate of Rise of On-State Current I _G =2xI _{GT} , tr≤100ns	F=120 Hz	T _J =125°C	dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms	T _J =25°C	V _{DSM} /V _{RSM}	V _{DRM} /V _{RRM} +100	V
Peak Gate Current	t _P =20µs	T _J =125°C	I_{GM}	4	Α
Average Gate Power Dissipation T _J =125°C		$P_{G(AV)}$	1	W	
Operating Junction Temperature		TJ	-40~+125	°C	
Storage Junction Temperature		T _{STG}	-40~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case (AC)	θјс	2.1	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J=25°C unless otherwise specified.)

FOR STANDARD TYPE (4 QUADRANTS)

	(1.40/15/0/11/10)									
PARAMETER	SYMBOL	TEST CONDITIONS		С			В			UNIT
PARAIVIETER	STIVIBUL			MIN	TYP	MAX	MIN	TYP	MAX	OINIT
Gate Trigger Current	ı		1-11-111			25			50	mA
(Note 1)	I _{GT}	V_D =12V, R_L =33 Ω	IV			50			100	mA
Gate Trigger Voltage	V_{GT}		ALL			1.3			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D=V_{DRM}$, $R_L=3.3k\Omega$, $T_J=125^{\circ}C$	ALL	0.2			0.2			V
Holding Current (Note 2)	I _H	I _T =500mA				25			50	mA
Latabia a Commant		I _G =1.2 I _{GT}				40			60	mA
Latching Current	Iι	IG-1.2 IGT	II			80			120	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DRM} , Gate Open, T _J =125°C		200			400			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation(Note 2)	(dV/dt)c	(dl/dt)c=7A/ms, T _J = 125°C		5			10			V/µs

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage(Note 2)	V_{TM}	I _{TM} =22.5A, t _p =380μs T _J =25°C				1.55	V
Threshold Voltage(Note 2)	V_{TO}		T _J =125°C			0.85	V
Dynamic Resistance(Note 2)	R_D		T _J =125°C			25	mΩ
Repetitive Peak Off-State Current	I_{DRM}	-	TJ=25°C			5	μΑ
	I_{RRM}	V _{DRM} =V _{RRM}	T _J =125°C			2	mA

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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