UNISONIC TECHNOLOGIES CO., LTD

BTB04 Preliminary TRIAC

SENSITIVE GATE TRIACS

DESCRIPTION

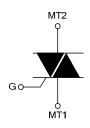
The UTC BTB04 is a 4A triacs, it uses UTC's advanced technology to provide customers with high commutation performances.

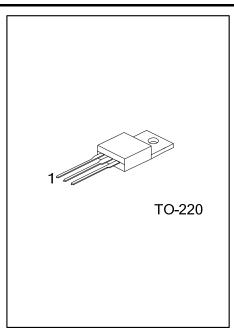
The UTC BTB04 is suitable for inductive loads, general purpose AC switching and an ON/OFF function in applications such as induction motor starting circuits, for phase control operation in light dimmers and static relays, etc.

FEATURES

- * Low gate trigger current
- * Low holding current

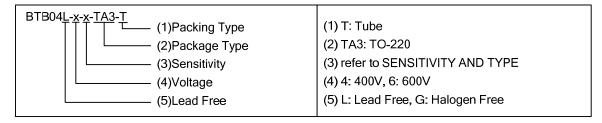
SYMBOL





ORDERING INFORMATION

Ordering	Dookogo	Pin .	Assignr	Dooking			
Lead Free Halogen Free		Package	1	2	3	Packing	
BTB04L-x-x-TA3-T	BTB04G-x-x-TA3-T	TO-220	MT1	MT2	G	Tube	

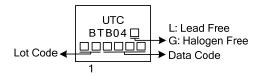


SENSITIVITY AND TYPE

	VOL1	ΓAGE	CENCITIVITY	TVDE		
PART NUMBER	400V	600V	SENSITIVITY	TYPE		
S		0	10mA	STANDARD		
D	0		5mA	STANDARD		
T	0	0	5mA	STANDARD		

⊚: Available

MARKING



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
RMS On-State Current (360° Conduction Angle)	T _C =90°C	I _{T(RMS)}	4	Α
Non Repetitive Surge Peak On-State	t _p =8.3ms	I _{TSM}	42	Α
Current (T _J initial=25°C)	t _p =10ms	· 13W	40	Α
I ² t Value	t _p =10ms	l ² t	8	A^2s
Critical Rate of Rise of On-State Current:	Repetitive F=50Hz	dI/dt	10	A/µs
I _G =50mA, dI _G /dt=0.1A/μs	Non Repetitive	dI/dt	50	A/µs
Repetitive Peak Off-State Voltage	400 T/D	\/ \^/	400	V
(T _J =110°C)	600 T/S	V_{DRM}/V_{RRM}	600	V
Peak Gate Current	t _p =20μs	I_{GM}	4	Α
Peak Positive Gate Voltage	t _p =20μs	V_{GM}	16	V
Peak Positive Gate Power Dissipation t _p =20µs		$P_{GM)}$	40	W
Average Gate Power Dissipation		$P_{G(AV)}$	1	W
Operating Junction Temperature	`	T_J	-40~+110	°C
Storage Junction Temperature		T_{STG}	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	60	°C/W	
Junction to Case for 360° Conduction Angle (F=50Hz) (AC)	0	2.4	°C/W	
Junction to Case (DC)	$\theta_{ m JC}$	3.2	°C/W	

■ ELECTRICAL CHARACTERISTICS

DADAMETED	CVMDOL	TEST COMPIL	TEST CONDITIONS		Т		D		S			Α			UNIT	
PARAMETER SYMBOL		TEST CONDITIONS		MIN	TYP	MAX	UNIT									
Gate Trigger	I _{GT}	V _D =12V (DC)	1-11-111			5			5			10			10	mΑ
Current	iGi	$R_L=33\Omega$	IV			5			10			10			25	mΑ
Gate Trigger Voltage	V_{GT}	T _J =25°C	ALL			1.5			1.5			1.5			1.5	V
Gate Non-Trigger Voltage	V_{GD}	$V_D = V_{DRM}$, $R_L = 3.3k\Omega$, $T_J = 110^{\circ}C$	ALL	0.2			0.2			0.2			0.2			٧
Time Gate Trigger	t _{GT}	$V_D=V_{DRM}$, $I_G=40mA$, $dI_G/dt=0.5A/\mu s$, $T_J=25^{\circ}C$	ALL		2			2			2			2		μs
Holding Current (Note 1)	l _H	I _T =100mA, Gate T _J =25°C	Open,			15			15			25			25	mA
Latching	ال	I _G =1.2I _{GT} ,	I-III-IV		10			10			20			20		mA
Current	_	T _J =25°C	II		20			20			40			40		mA
Peak On-State Voltage (Note 1)	V _{TM}	I _{TM} =5.5A, t _p =380 T _J =25°C	μs,			1.65			1.65			1.65			1.65	V
Repetitive	I _{DRM}	V _{DRM} Rated, T _J =25°C				0.01			0.01			0.01			0.01	mA
Peak Off-State Current	I _{RRM}	V _{RRM} Rated, T _J =				0.75			0.75			0.75			0.75	mA
Critical Rate of Rise of Off-State Voltage (Note 1)	dV/dt	Linear Slope up t V _D =67%V _{DRM} , Ga Open, T _J =110°C			10			10		10			10			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation (Note 1)	(dV/dt)c	(dI/dt)c=1.8A/ms, T _J =110°C			1			1			5			5		V/µs

Note: For either polarity of electrode MT2 voltage with reference to electrode MT1.

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