URFP150 Preliminary Power MOSFET

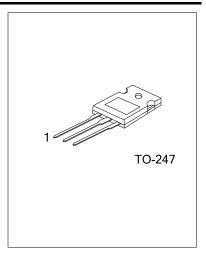
# 41A, 100V N-CHANNEL POWER MOSFET

### **■** DESCRIPTION

The UTC **URFP150** is an N-channel enhancement MOSFET using UTC's advanced technology to provide the customers with a minimum on-state resistance and high switching speed.

#### **■ FEATURES**

- \*  $R_{DS(ON)}$ <55m $\Omega$  @  $V_{GS}$ =10V, $I_D$ =25A
- \* High Switching Speed



### **■ ORDERING INFORMATION**

Ordering Number		Dookses	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
URFP150L-T47-T	URFP150G-T47-T	TO-247	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source

URFP150L-T47-T

(1)Packing Type
(2) T47: TO-247

(3)Lead Free

(3) G: Halogen Free, L: Lead Free

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## **■ ABSOLUTE MAXIMUM RATINGS**

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	100	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current	Continuous	I <sub>D</sub>	41	Α
	Pulsed	I <sub>DM</sub>	160	А
Avalanche Current		I <sub>AR</sub>	41	Α
Single Pulsed Avalanche Energy (Note 2)		E <sub>AS</sub>	830	mJ
Power Dissipation		$P_D$	192	W
Junction Temperature		T <sub>J</sub>	-55~+150	°C
Storage Temperature		T <sub>STG</sub>	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. L = 740  $\mu$  H, I<sub>AS</sub> = 41A, V<sub>DD</sub> = 25V, R<sub>G</sub> = 25  $\Omega$ 

## **■ ELECTRICAL CHARACTERISTICS**

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS		OTMBOL	TEST SOMETHIONS	IVIIIV		1017 0 1	OTT
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250µA	100			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =80V			10	μA
Gate-Source Leakage Current	Forward	I <sub>GSS</sub>	V <sub>GS</sub> =+20V			+100	nA
	Reverse		V <sub>GS</sub> =-20V			-100	nA
ON CHARACTERISTICS			, 55	- I	ı	ı	
Gate Threshold Voltage		$V_{GS(TH)}$	I <sub>D</sub> =250μA	2		4	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =25A			55	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		$C_{ISS}$	\\ -0\\ \\ -25\\		2800		pF
Output Capacitance		Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, -f=1.0MHz		1100		pF
Reverse Transfer Capacitance		$C_{RSS}$	I = 1.0WHZ		280		pF
SWITCHING PARAMETERS							
Total Gate Charge		$Q_G$	\/ -F0\/ \/ -10\/			140	nC
Gate to Source Charge		$Q_GS$	V <sub>DD</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =41A, I <sub>G</sub> =100µA,			29	nC
Gate to Drain Charge		$Q_GD$	ID-4 IA, IG-100μA,			68	nC
Turn-ON Delay Time		$t_{D(ON)}$			16		ns
Rise Time		$t_R$	$V_{DD}$ =30V, $I_{D}$ =0.5A, $R_{G}$ =25 $\Omega$ ,		120		ns
Turn-OFF Delay Time		$t_{D(OFF)}$	V <sub>GS</sub> =0~10V		60		ns
Fall-Time		$t_{F}$			81		ns
SOURCE- DRAIN DIODE RATIN	IGS AND (	CHARACTERI	STICS				
Maximum Body-Diode Continuous Current		$I_S$				41	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				160	Α
Drain-Source Diode Forward Voltage		V <sub>SD</sub>	I <sub>S</sub> =41A			2.5	V

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