

## 10N60K-MT

Power MOSFET

10A, 600V N-CHANNEL  
POWER MOSFET

## ■ DESCRIPTION

The **UTC 10N60K-MT** is a high voltage and high current power MOSFET, designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

## ■ FEATURES

- \*  $R_{DS(ON)} < 0.75\Omega$  @  $V_{GS} = 10V$ ,  $I_D = 5.0A$

- \* Low gate charge ( typical 33 nC)

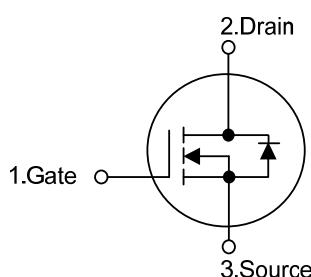
- \* Low Crss ( typical 18 pF)

- \* Fast switching

- \* 100% avalanche tested

- \* Improved dv/dt capability

## ■ SYMBOL



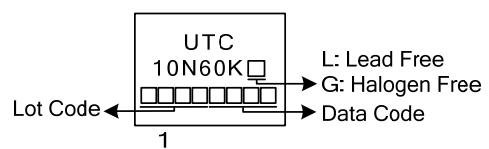
## ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
10N60KL-TA3-T	10N60KG-TA3-T	TO-220	G	D	S	Tube
10N60KL-TF3-T	10N60KG-TF3-T	TO-220F	G	D	S	Tube
10N60KL-TF1-T	10N60KG-TF1-T	TO-220F1	G	D	S	Tube
10N60KL-TF2-T	10N60KG-TF2-T	TO-220F2	G	D	S	Tube
10N60KL-TF3T-T	10N60KG-TF3T-T	TO-220F3	G	D	S	Tube
10N60KL-T2Q-T	10N60KG-T2Q-T	TO-262	G	D	S	Tube
10N60KL-TQ2-T	10N60KG-TQ2-T	TO-263	G	D	S	Tube
10N60KL-TQ2-R	10N60KG-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

10N60KL-TA3-T	(1) Packing Type (2) Package Type (3) Green Package	(1) T: Tube (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1 TF2: TO-220F2, TF3T: TO-220F3, T2Q: TO-262, TQ2: TO-263 (3) L: Lead Free, G: Halogen Free and Lead Free
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## ■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	600	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Avalanche Current (Note 2)		$I_{AR}$	10	A
Drain Current	Continuous	$I_D$	10	A
	Pulsed (Note 2)	$I_{DM}$	38	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	200	mJ
	Repetitive (Note 2)	$E_{AR}$	12	mJ
Peak Diode Recovery $dv/dt$ (Note 4)		$dv/dt$	4.5	V/ns
Power Dissipation	TO-220/TO-262	$P_D$	156	W
	TO-263		52	W
	TO-220F/TO-220F1			
	TO-220F2/TO-220F3			
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Operating Temperature		$T_{OPR}$	-55 ~ +150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3.  $L = 4\text{mH}$ ,  $I_{AS} = 10\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25 \Omega$  Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 9.5\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		$\theta_{JA}$	62.5	$^\circ\text{C/W}$
Junction to Case	TO-220/TO-262	$\theta_{JC}$	0.8	$^\circ\text{C/W}$
	TO-263		2.4	$^\circ\text{C/W}$
	TO-220F/TO-220F1			
	TO-220F2/TO-220F3			

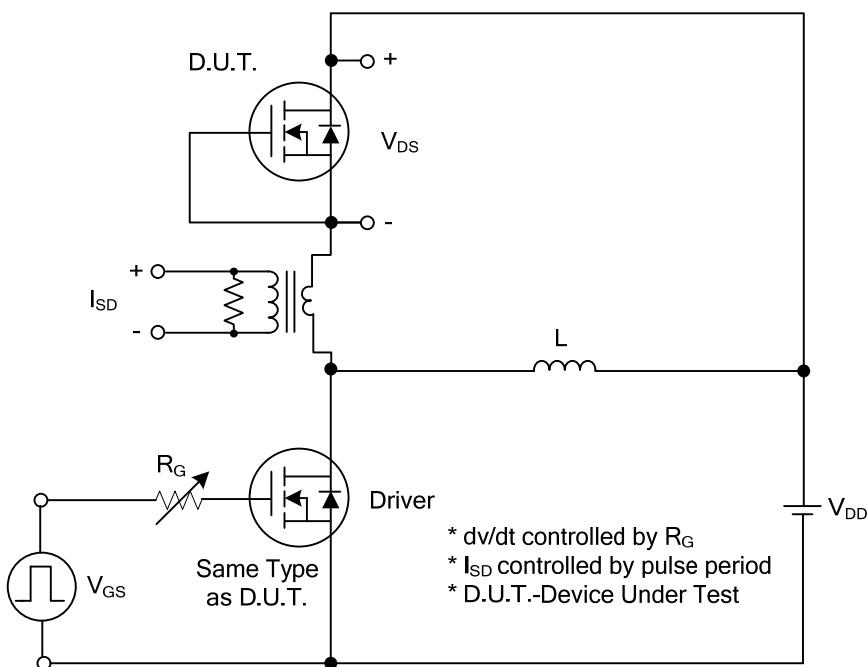
■ ELECTRICAL CHARACTERISTICS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	600			V
Drain-Source Leakage Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=600\text{V}, \text{V}_{\text{GS}}=0\text{V}$		1		$\mu\text{A}$
Gate-Source Leakage Current	Forward	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100		nA
	Reverse	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100		nA
Breakdown Voltage Temperature Coefficient	$\Delta\text{BV}_{\text{DSS}}/\Delta T_J$	$\text{I}_D=250 \mu\text{A}$ , Referenced to $25^\circ\text{C}$		0.7		$\text{V}/^\circ\text{C}$
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=5.0\text{A}$		0.63	0.75	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$\text{C}_{\text{ISS}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0 \text{ MHz}$		1570		pF
Output Capacitance	$\text{C}_{\text{OSS}}$			166		pF
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$			18		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge	$\text{Q}_G$	$\text{V}_{\text{DS}}=50\text{V}, \text{I}_D=1.3\text{A}, \text{V}_{\text{GS}}=10\text{V}$ $\text{I}_G=100\mu\text{A}$ (Note1, 2)		33		nC
Gate-Source Charge	$\text{Q}_{\text{GS}}$			9		nC
Gate-Drain Charge	$\text{Q}_{\text{GD}}$			8.5		nC
Turn-On Delay Time	$t_{\text{D(ON)}}$	$\text{V}_{\text{DD}}=30\text{V}, \text{I}_D=0.5\text{A}, \text{R}_G=25\Omega,$ $\text{V}_{\text{GS}}=0\text{V}$ (Note1, 2)		67		ns
Turn-On Rise Time	$t_R$			84		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			205		ns
Turn-Off Fall Time	$t_F$			95		ns
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$\text{I}_S$				10	A
Maximum Pulsed Drain-Source Diode Forward Current	$\text{I}_{\text{SM}}$				38	A
Drain-Source Diode Forward Voltage	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=10\text{A}$			1.4	V
Reverse Recovery Time	$t_{\text{rr}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=10\text{A},$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$ (Note1)		420		ns
Reverse Recovery Charge	$\text{Q}_{\text{RR}}$			4.2		$\mu\text{C}$

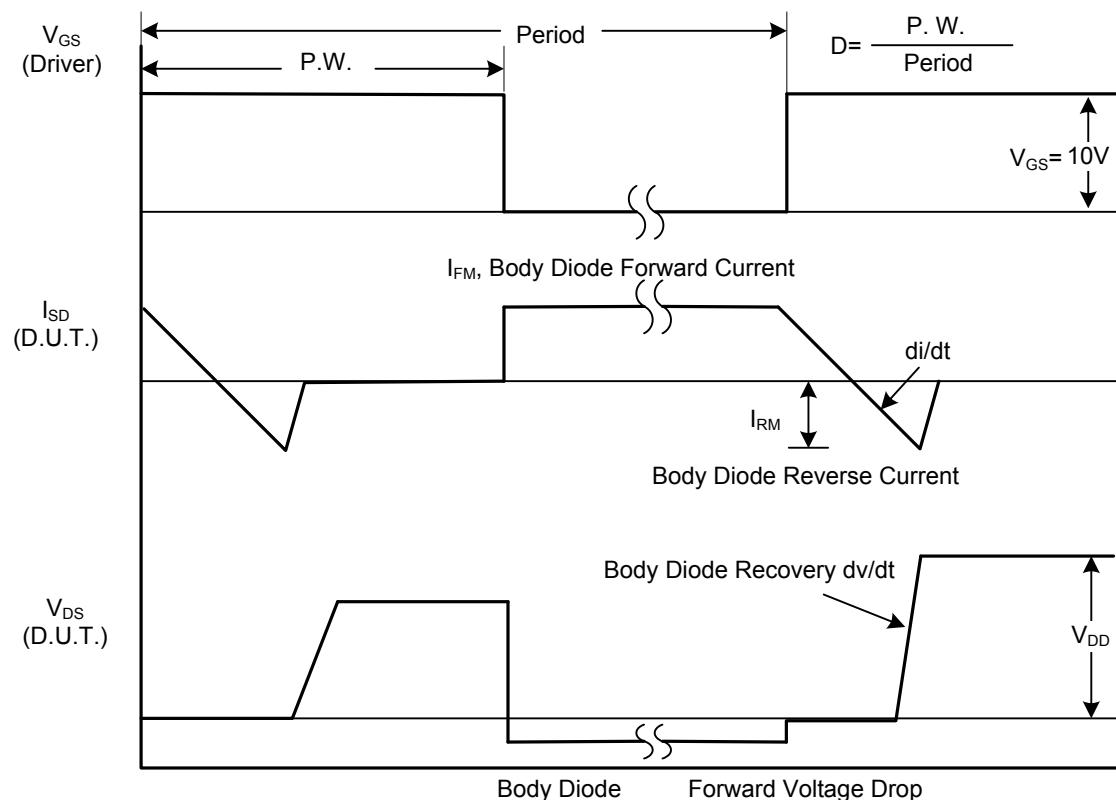
Notes: 1. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ 

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

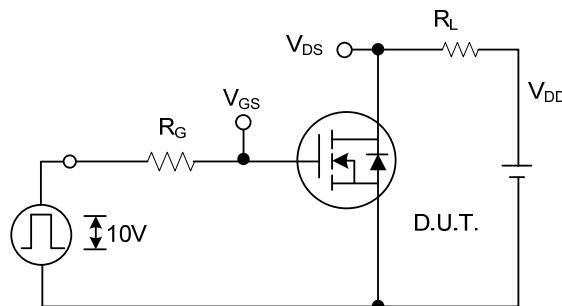


Peak Diode Recovery dv/dt Test Circuit

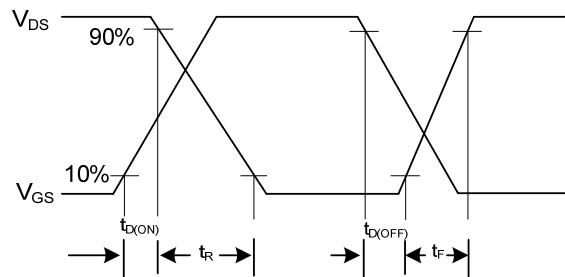


Peak Diode Recovery dv/dt Waveforms

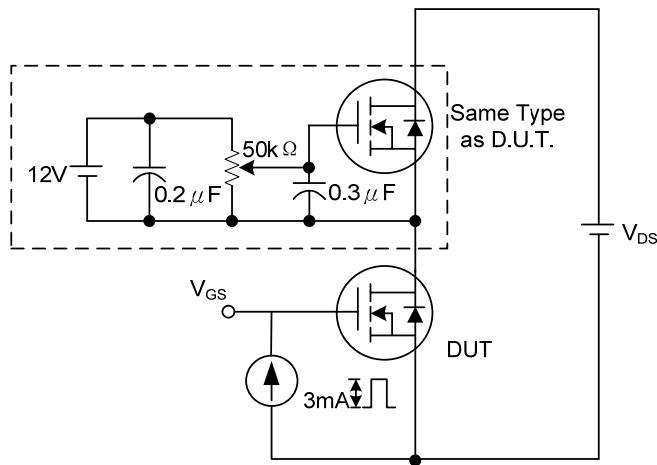
### ■ TEST CIRCUITS AND WAVEFORMS (Cont.)



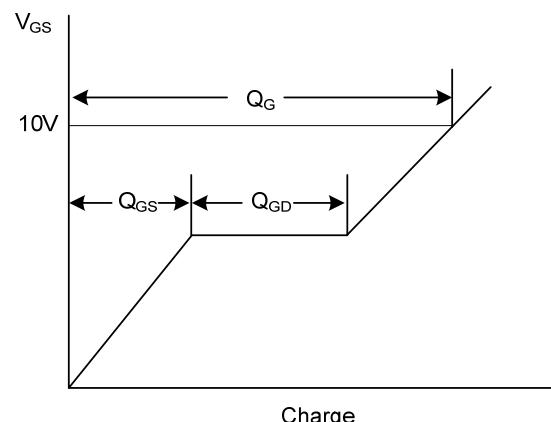
**Switching Test Circuit**



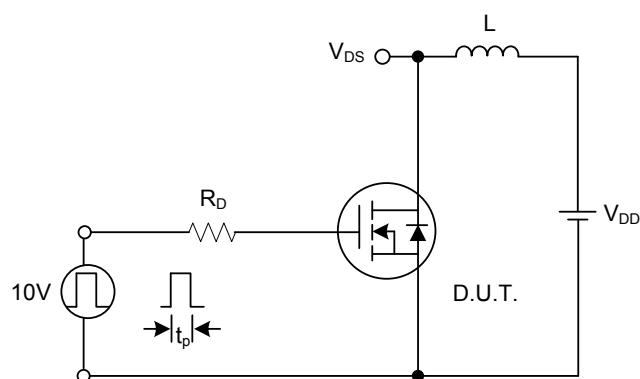
**Switching Waveforms**



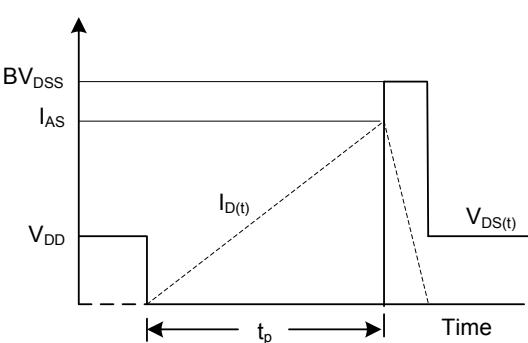
**Gate Charge Test Circuit**



**Gate Charge Waveform**

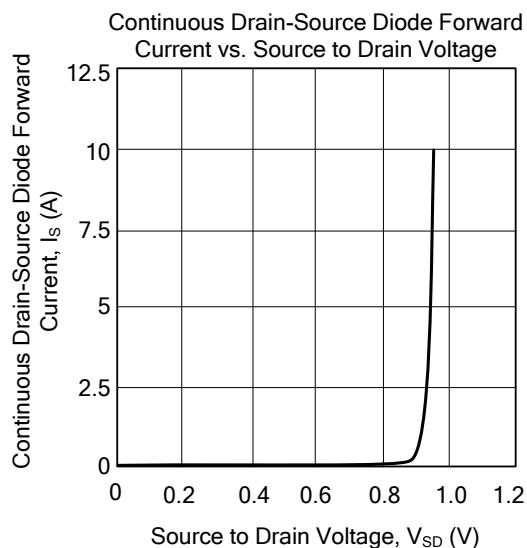
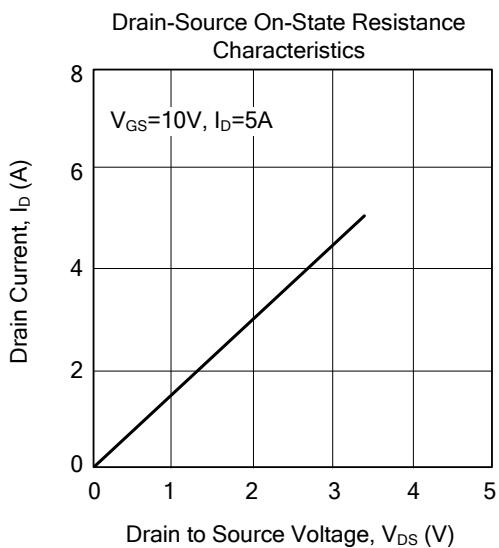
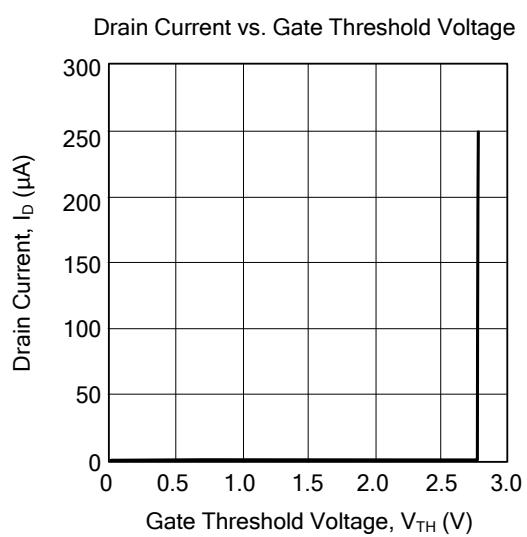
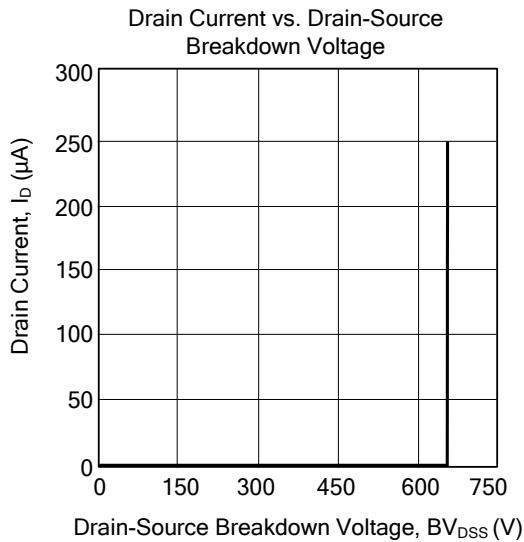


**Unclamped Inductive Switching Test Circuit**



**Unclamped Inductive Switching Waveforms**

■ TYPICAL CHARACTERISTICS



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