

# BA3308

# LINEAR INTEGRATED CIRCUIT

# DUAL PREAMPLIFIER WITH ALC

# DESCRIPTION

The UTC **BA3308** is designed to have dual preamplifier ICs with built – in ALC circuits for use in stereo amplification. The preamplifiers have high gain and low distortion. A built-in rectifier for ALC circuit implies good channel balance and large dynamic range can be constructed with addition of just an external time constant circuit.

# FEATURES

\* Wide operating power supply voltage range

 $(V_{CC} = 4.5V \sim 14V)$ 

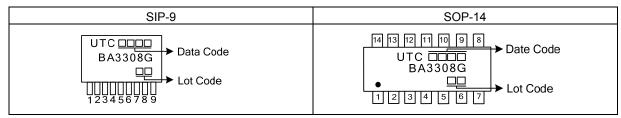
- \* Power-on mute circuit to avoid "pop" noise generation.
- \* No input coupling capacitors are necessary
- \* High gain (G<sub>VO</sub>=80dB)and low noise (V<sub>NIN</sub>=1 $\mu$ Vrms)
- \* Low distortion (THD=0.1%)
- \* Good ALC channel balance with built-in ALC rectifier diode
- \* Adjustable ALC dynamic range by external input resistor.

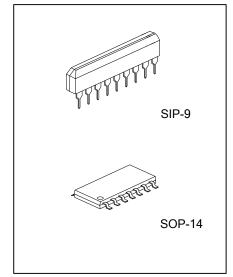
### ORDERING INFORMATION

Ordering Number	Package	Packing
BA3308G-G09-T	SIP-9	Tube
BA3308G-S14-R	SOP-14	Tape Reel

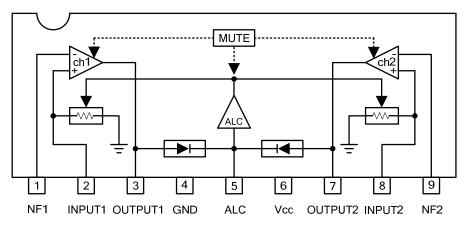
ВАЗ308 <u>Ģ-<u>G09</u>-Ţ</u>			
	(1)Packing Type	(1) R: Tape Reel, T: Tube	
	(2)Package Type	(2) G09: SIP-9, S14: SOP-14	
	(3)Green Package	(3) G: Halogen Free and Lead Free	
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#### MARKING

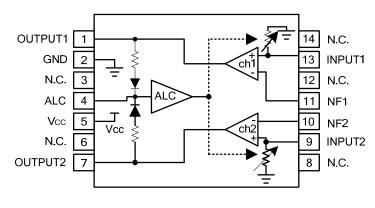




# BLOCK DIAGRAM



SIP-9



SOP-14



# ■ ABSOLUTE MAXIMUM RATING (T<sub>A</sub> = 25°C)

PARAMETER		SYMBOL	RATINGS	UNIT	
Power Supply Voltage		V <sub>cc</sub>	16	V	
Power Dissipation	SIP-9		950	mW	
	SOP-14	- P <sub>D</sub>	450	mW	
	SIP-9		9.5	°C/mW	
Derating above Ta = $25^{\circ}$ C	SOP-14		4.5	°C/mW	
Operating Temperature		T <sub>OPR</sub>	0 ~ +85	°C	
Storage Temperature		T <sub>STG</sub>	-65 ~ +125	°C	

## ■ **RECOMMENDED OPERATING CONDITIONS** (T<sub>A</sub> = 25°C)

PARAMETER S	SYMBOL	RATINGS	UNIT
Power Supply Voltage	Vcc	V <sub>CC</sub> +4.5~ +14	

Note: This IC is not designed to be radiation-resistant.

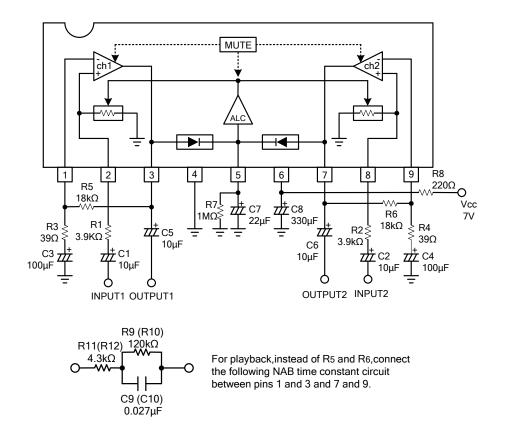
## ELECTRICAL CHARACTERISTICS

(T<sub>A</sub> = 25°C, V<sub>CC</sub> =7.0V, f =1kHz and BPF: 20Hz ~ 20kHz, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Maximum Output Voltage V <sub>OM</sub>		THD=1%	0.6	1.2		V <sub>rms</sub>
Input Conversion Noise Voltage	V <sub>NIN</sub>	Conversion with $R_g$ =2.2k $\Omega$ and NAB34dB at 1kHz		1.0	2.0	μV <sub>rms</sub>
Quiescent Current	lq	V <sub>IN</sub> =0Vrms	1.5	3.3	4.5	mA
Input Resistance	R <sub>IN</sub>		15	31.5	45	kΩ
Total Harmonic Distortion	THD	NAB34dB, V <sub>OUT</sub> =40mV <sub>rms</sub>		0.1	0.3	%
Open Loop Voltage Gain	G <sub>VO</sub>	V <sub>OUT</sub> = -10dBV	70	80		dB
ALC Range	ALC	R <sub>G</sub> =3.9kΩ, V <sub>IN</sub> =−70dBV reference, THD=3%	40	70		dB
ALC Channel Balance	∆ALC	V <sub>IN</sub> = -60dBV, -30dBV		0	2.5	dB
Channel Separation	CS	V <sub>O</sub> =0dBV, NAB34dB	60	75		dB



# TYPICAL APPLICATION CIRCUIT



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